

12/03/01

Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

PATENT DATE

FA  
SCANNED 746 ① Q.A. MT

APPLICATION NO. 09/998326	CONT/PRIOR F	CLASS 257.14	SUBCLASS 724	ART UNIT -2811	EXAMINER TON
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## APPLICANTS

Shunsuke Endou

2133

**TITLE**

Semiconductor device capable of easily setting test mode during test conducted by applying high voltage

PTO-2040  
12/99**ISSUING CLASSIFICATION**

ORIGINAL						CROSS REFERENCE(S)								
CLASS			SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION														

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  _____  _____	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
				_____ (Primary Examiner) (Date)	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
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